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SEMICONDUCTOR DEVICE

Inventor:	Yukihide Nose, Matsushita Denki Sangyo K.K., 1006 Oaza-kadoma, Kadoma-shi
Applicant:	Matsushita Denki Sangyo K.K., 1006 Oaza-kadoma, Kadoma-shi
Agents:	Toshio Nakao, patent attorney, and 1 other

[There are no amendments to this patent.]

Claims

1. Semiconductor device characterized in that multiple semiconductor elements adhered with their wiring surfaces facing each other are contained in a single package, the circuit functions of two semiconductor elements are the same, and common leads of the two elements are drawn out from the adhered surfaces of the two elements.

2. The semiconductor device described in Claim 1, characterized in that electrode terminal patterns of two semiconductor elements provide plane symmetry.

3. The semiconductor device described in Claim 1 or 2, characterized in that common leads of two semiconductor elements are made from thin sheets of copper, iron, iron/nickel alloy, or iron/nickel/cobalt alloy, with their surfaces being plated with gold or silver.

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